

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,130,224 B2  
APPLICATION NO. : 10/522316  
DATED : October 31, 2006  
INVENTOR(S) : Katsutoshi Moriyama et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**Title Page:**

Item (54) should read as follows:

-- COMPOUND STORAGE CIRCUIT AND SEMICONDUCTOR DEVICE  
INCLUDING COMPOUND STORAGE CIRCUIT --.

Signed and Sealed this

Tenth Day of July, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*